KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE, WARANGAL-15
(An Autonomous Institute under Kakatiya University, Warangal)
DEPARTMENT OF ELECTRONICS & INSTRUMENTATION ENGINEERING

Minutes of Virtual BoS Meeting

Date: 14.06.2020
Time: 10:30AM

Agenda:
1. Approval of Scheme of Instruction & Evaluation for B. Tech (EIE) - V, VI, VII & VIII Semesters under URR-18.
2. Approval of Syllabi of B. Tech. (EIE) - V & VI Semesters courses under URR-18.
3. Approval of Syllabi for service courses offered by Dept. of EIE to B. Tech (ECE), B. Tech. (CSE) & B. Tech. (EEE) programmes under URR-18.
4. Any other item with the permission of chair

Members Present:

Virtual BoS meeting of was conducted on 14.06.2020, from 10:30 am to 1:00 pm through ZOOM application and the following members were present during the meeting

S. No.	Name of the Member	Designation	Position in BoS	Signature
1.	Dr. K. Venu Madhav	Professor & HoD, EIED, KITSW	Chairperson, BoS	
2.	Dr. K. Sivani	Senior Professor of EIED, KITSW	Member	
3.	Prof. M. Sreelatha	Professor of EIED, KITSW	Member	
4	Smt. R. Nirmala Devi	Assoc. Prof. of EIED, KITSW	Member	
5.	Sri O. Anjaneyulu	Assoc. Prof. of EIED, KITSW	Member	
6.	Dr. P. Laxminarayana	Director, NERTU, Osmania University, Hyderabad	External Member (from renowned Academic Institute)	Approved through email
7.	Dr. T. Kishore Kumar	Professor of ECE, NIT, Warangal	External Member (from renowned Academic Institute)	Approved through email
8.	Dr. K. Bikshalu	Asst. Prof. of ECE, University College of Engineering, KU, Kothagudem	External Member (University Nominee)	Approved through email
9.	Sri T. Srinivas	CEO, Sumith Electronics Pvt. Ltd., Hyderabad	External Member (from Industry)	Approved through email
10.	Dr. Y. Jagan Mohan Reddy	Senior Engineering Manager, Honeywell Technology Solutions Lab Pvt. Ltd., Hyderabad	External Member (from Industry)	Approved through email
11.	Sri R. Dileep	Senior ASIC design Engineer, Synopsis Inc. Pvt. Ltd., Hyderabad	External Member (from Industry)	Approved through email
12.	Sri R. Divyanand	Senior Engineer, Qualcomm Technology Inc., Hyderabad	External Member (Post Graduate Meritorious Alumnus– Academia/Industry)	Approved through email

13.	Sri M. Raghuram	Assoc. Prof. of EIED, KITSW	Co-OptedMember-1	
14.	Sri B. Shashikanth	Asst. Prof. of EIED, KITSW	Co-OptedMember-2	
15.	Sri B. Jeevan	Asst. Prof. of EIED, KITSW	Co-OptedMember-3	

Resolutions:

1. Prof. K. Venu Madhav, *HoD-EIE & Chairperson BoS-EIE* welcomed all the external members to the virtual meeting and introduced all the internal members of BoS-EIE.

2. Chairperson BoS-EIE briefed the agenda points to be discussed during the BoS meeting.

3. All the external BoS members participated in the meeting and offered their valuable suggestions on:

   (i) Scheme of Instruction & Evaluation for B. Tech. (EIE) - V, VI, VII & VIII Semesters under URR-18

   (ii) Syllabi of B. Tech. (EIE) - V & VI Semesters courses

   • Syllabi of service courses offered by Dept. of EIE to B. Tech (ECE), B. Tech. (CSE) & B. Tech. (EEE) programmes

4. As per External BoS members' suggestions, the modifications were incorporated in the scheme & syllabi and the same are tabulated below:

   (i) Suggestions & Modifications incorporated in Scheme of Instruction & Evaluation for B. Tech. (EIE) - V, VI, VII & VIII Semesters:

S. No.	Course Code	Course Name	Suggestions made by the BoS members	Remarks
1.	U18EI703B	Artificial Intelligence & Machine Learning	Artificial Intelligence and Machine learning courses are to be offered seperately	Couldn't incorporate as suggested because of inability of removing the other core courses
2.	U18EI802B	Web Technologies	Can be removed and in that place, a course on Wireless and Mobile Communications to be offered	Incorporated the course titled "Wireless and Mobile Communications" (*Introduction to emerging technologies in communication networks shall be placed as 1 unit*)

   (ii) Suggestions & Modifications incorporated in Syllabi of courses for B. Tech. (EIE) - V & VI Semesters:

S. No.	Course Code	Course Name	Suggestions made by the BoS members	Remarks
V SEMESTER:				
1.	U18EI502	Professional Elective - I / MOOC-I (a) U18EI502A: Communication Systems (b) U18EI502B: Computer Architecture And Organization	Members unanimously approved the syllabus without modifications	-

S.No.	Course Code	Course Name		Suggestions from the members	Remarks
2.	U18 EI 503	Linear Integrated Circuits and Applications			
3.	U18 EI 504	Microprocessor Systems and Interfacing	Reference book with content of 8085 Microprocessors (by R. Gaonkar) to be included		Incorporated
4.	U18 EI505	VLSI Design	Members unanimously approved the syllabus without modifications		-
5.	U18 EE511	Linear Control Systems			
6.	U18 EI 507	Linear Integrated Circuits Laboratory			
7.	U18 EI 508	Virtual Instrumentation Laboratory	Members unanimously approved the syllabus without modifications		-
8.	U18 EI 509	VLSI Design Laboratory			
9.	U18 EI 510	Seminar			
VI SEMESTER:					
10.	U18EI603	Professional Elective - II / MOOC-II (a) U18EI603A: Electro Magnetic Theory (b) U18EI603B: Analytical Instrumentation	Members unanimously approved the syllabus without modifications		-
11.	U18EI604	Digital Signal Processing & Applications			
12.	U18EI605	Microcontrollers & Embedded Systems			
13.	U18EI606	Process Control			
14.	U18EI607	Signal Processing Laboratory			
15.	U18EI608	Microprocessors and Microcontrollers Laboratory			
16.	U18EI609	Process Control Laboratory			
17.	U18EI610	Mini Project			

(iii) **Suggestions & Modifications incorporated in Syllabi of service courses offered by Dept. of EIE to B. Tech. (EEE) & B. Tech. (ECE) programmes:**

S. No.	Course Code	Course Name	Serviced Branch	Suggestions from the members	Remarks
1.	U18EC502C	Electronic Measurements & Instrumentation	ECE	Members unanimously approved the syllabus without	-
2.	U18EC603C	Biomedical Instrumentation			

				modifications	
3.	U18EI611	Microprocessor & Interfacing			
4.	U18EI612	Digital Electronics & Microprocessors Laboratory	CSE		
5.	U18EI614	Signals and Linear Systems	EEE		

5. Other important suggestions given by the external BoS members:

S. No.	Suggestions made by the external BoS members	Remarks
1.	*Prof. T. Kishore Kumar, ECED, NITW:* In Signal Processing Laboratory, Computational efficiency of DFT & FFT programs are to analyzed	Course faculty will be guided in this direction
2.	*Prof. P. Laxminarayana, Director, NERTU-OU:* A course on Instruments for Medical Electronics/ Hospitals and Chemical Technology/ Bio Technology can be introduced	Invited lectures will be planned by department to give required inputs
3.	*Prof. P. Laxminarayana, Director, NERTU-OU:* A project group of 4 to 5 students from II year to IV year can be formed and encourage the batch to take up multidisciplinary projects aiming for product development	Department is ready to plan for these type of projects with enthusiastic student groups
4.	*Dr. K. Bikshalu, ECED, UCE, KU:* In EMT course, Importance of Transmission lines is to be focused	Course faculty will be guided in this direction
5.	*Dr. Y. Jagan Mohan Reddy, Sr. Engg. Manager, Honeywell:* More inputs are to be given for students in the area of Industrial Automation	In the Industrial Automation course, the content of PLC, SCADA & DCS is introduced as basic requirement to expose to students. However, in the course titled, PC based Instrumentation to be offered in VIII semester, detailed contents required for industry requirements will be placed
6.	*Sri T. Srinivas, CEO, Sumith Electronics Pvt. Ltd.,:* Mini & major project works are to be effectively carried out by the students	Faculty are taking personal interest for project works & students are instructed to do the project works in department laboratories as every lab is well equipped with latest software & hardware
7.	*Sri R. Divyanand, Sr. Engineer, Qualcomm:* Course learning platforms in latest technologies are to be exposed for the students to learn content beyond the syllabus	(a) Institute is encouraging all the students to learn through SWAYAM & Course era platforms (b) Department has CoE – LabVIEW Academy and encouraging the II, III & IV year students to get CLAD certification
8.	*Sri R. Dileep, Sr. ASIC Design Engineer, Synopsis:* VERILOG is to be used instead of VHDL during VLSI design theory and lab courses	Good number of experiments are placed in VLSI lab syllabus which use VERILOG instead of VHDL

Meeting was concluded by thanking all the BoS members, Principal, AAC members and faculty of EIE Department for giving their suggestions while preparing the scheme and drafting the syllabus.

The meeting was adjourned at 1:00pm

Regards: Sd/—

Dr. K. Venu Madhav

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